AMENDMENTS

In the Claims:

1. (CURRENTLY AMENDED) A method for creating a narrow linewidth hybrid semiconductor laser comprising:

soldering a semiconductor optical gain chip to a micromachined silicon bench to create an internal element of said laser; and

coupling said optical gain chip to a ~~silicon-dioxide-and~~ silicon-oxynitride based waveguide, ~~terminating~~ wherein said waveguide terminates in an external feedback element, said step of coupling further comprises:

using a flip-chip aligner-bonder to horizontally align the coupling of said gain chip to said waveguide; and

using a plurality of micromachined stand-offs to vertically align the coupling of said gain chip to said waveguide.

B1

2. (PREVIOSULY AMENDED) The method of claim 1 wherein said external feedback element comprises Bragg gratings.

3. (ORIGINAL) The method of claim 2 wherein said Bragg gratings are formed by the coupling of a first Bragg grating and a second Bragg grating to a main waveguide trunk.

4. (ORIGINAL) The method of claim 3 wherein said first Bragg grating and said second Bragg grating are formed by the periodic variation of the refractive index of said first Bragg grating and said second Bragg grating.

5. (CANCELED)

6. (AMENDED) The method of claim 1 wherein ~~said narrow~~ linewidth of said hybrid semiconductor is in the tens of kHz range.

7. (CANCELED)

B' contd

8. (CURRENTLY AMENDED) The method of claim 1 wherein ~~said step of coupling is achieved in~~ said optical gain chip and said waveguide are miniature ~~micromachined~~ units made by a micromachine process.

9. (PREVIOSULY AMENDED) The method of claim 1 wherein said waveguide further comprises:

a first layer of silicon-dioxide;

a layer of silicon-oxinitride; and

a second layer of silicon-dioxide.

10. (CANCELED)

11. (PREVIOSULY AMENDED) The method of claim 9 wherein the interface between said first layer and said silicon-oxinitride layer and the interface between said second layer and said silicon-oxinitride layer are coated with an antireflection coating in order to further reduce loss and scattering at said interface.

12. (CURRENTLY AMENDED) The method of claim 3 further comprises ~~wherein~~ matching said waveguide ~~is tailored to match~~ with said gain chip in order to further reduce loss due to mismatch of modes of said waveguide and said gain chip.

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13. (CANCELED)

14. (CANCELED)

15. (CANCELED)

Claims 16-30 (CANCELED).